



Camelback Computer Architecture, LLC Consulting Services

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OBJECTIVE	Technical consulting position for planning, implementing, validating, and optimizing microprocessor-based products. Open to full- or part-time; locations in Arizona and California preferred.
SKILLS	<p>Product Planning</p> <ul style="list-style-type: none">• Working with customers, engineers, product marketing to define product features and implementation plan• Familiar with industry hardware and software standards (e.g., PCI-E, HT, USB, ACPI) <p>Performance Evaluation</p> <ul style="list-style-type: none">• Microbenchmarks• Standard system benchmarks (SPECcpu, SPECsfs, TPC)• Optimize code using V-Tune and other performance monitoring tools <p>Validation</p> <ul style="list-style-type: none">• Focused and random test generation• PC compatibility validation• Pre- and post-silicon <p>Programming</p> <ul style="list-style-type: none">• High-level languages (C, FORTRAN)• Assembly language (e.g., x86, ARM, MIPS) <p>Communication</p> <ul style="list-style-type: none">• Writing technical documentation• Presentations for engineering/business review and decisions• Lectures for instruction
SAMPLE PROJECTS	<p>1989-2002 Led technical definition of Pentium™ Processor and 815 chipset Led microarchitecture definition and validation of Itanium™ Processor</p> <p>2004 Developed proof-of-concept demonstration for communication protocol on ARM development board using PC-based development tools</p> <p>2005-2006 Evaluated VLSI design of proprietary search algorithm for networking chips Presented cost/performance/competitive analysis to executive team for business decision</p> <p>2005-2007 Refined microarchitecture definition for low-power PC processor Documented for internal training and patent applications</p>